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Sheet 1 of 1

Form 1449*	Atty. Docket No.: 303.466US1	Serial No. 09/069,668
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Kie Y. Ahn et al.	
	Filing Date: April 29, 1998	Group: 2814

U. S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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OTHER DOCUMENTS

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	Hamakawa, Y., et al., "Optoelectronics and Photovoltaic Applications of Microcrystalline SiC", <u>Materials Research Society Symposium Proceedings</u> , 164, Boston, MA, 291-301, (11/29-12/1, 1989)
	Mohajerzadeh, S., et al., "A Low Energy Ion Beam Assisted Deposition Technique for Realizing iso-type SiGe/Si hetero-interface diodes", <u>Thin Solid Films</u> , 281, 182-187, (1996)
	Mohajerzadeh, S., et al., "A Low-Temperature Ion Vapor Deposition Technique for Silicon and Silicon-Germanium Epitaxy", <u>Canadian J. Physics</u> , 74, S69-S73, (1996)
	Mohri, M., et al., "Effect of SiF(4)/SiH(4)/H(2) Flow Rates on Film Properties of Low-Temperature Polycrystalline Silicon Films Prepared by Plasma Enhanced Chemical Vapor Deposition", <u>IEICE Transactions on Electronics</u> , E77-C, 1677-1684, (Oct. 1994)

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Examiner Millie D. Perl

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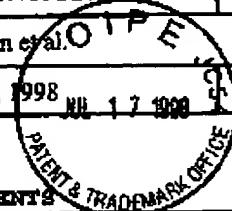
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Examiner <u>Mildred J. Col</u>	Date Considered 13 JANUARY 2002
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Examiner Wiltshire, J.C.

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<i>u</i>	Walczak, F., et al., "Tailoring Interfacial Oxide for Polysilicon Bit-Cell Contacts and Emitters with In Situ Vapor HF Interface Cleaning and Polysilicon Deposition in a 4Mbit BiCMOS Fast Static RAM", <u>Proc. IEEE Bipolar Circuits and Technology Meeting</u> , Minneapolis, MN, 84-87, (1992)
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